



6HVRC SERIES

3 to 50kV, 2A, Standard Recovery
Rectifier Blocks with Insert Mounting



Features

- Medium Power
- Single and 2-Leg Versions, Resistor Capacitor Compensated
- Epoxy Molded Encapsulation with Mounting Inserts
- Threaded Brass Solder Terminal Inserts

Specifications¹

Part Number ²	V_{RRM}^3 V	I_{FAVM}^3 A	V_F^3 V	I_R μA	I_{FSM} A	L in.
Common Anode (N) Center Tap Subseries (Figure 1)						
6HVN5RC	5000	2	5	500	400	5.50
6HVN8RC	8000	2	8	500	400	8.25
6HVN10RC	10000	2	10	500	400	10.25
6HVN12RC	12000	2	12	500	400	13.25
6HVN15RC	15000	2	15	500	400	13.25
6HVN20RC	20000	2	20	500	400	25.00
6HVN25RC	25000	2	25	500	400	25.00
Common Cathode (P) Center Tap Subseries (Figure 2)						
6HVP5RC	5000	2	5	500	400	5.50
6HVP8RC	8000	2	8	500	400	8.25
6HVP10RC	10000	2	10	500	400	10.25
6HVP12RC	12000	2	12	500	400	13.25
6HVP15RC	15000	2	15	500	400	13.25
6HVP20RC	20000	2	20	500	400	25.00
6HVP25RC	25000	2	25	500	400	25.00
Doubler-Style Center Tap Subseries (Figure 3)						
6HVD5RC	5000	2	5	500	400	5.50
6HVD8RC	8000	2	8	500	400	8.25
6HVD10RC	10000	2	10	500	400	10.25
6HVD12RC	12000	2	12	500	400	13.25
6HVD15RC	15000	2	15	500	400	13.25
6HVD20RC	20000	2	20	500	400	25.00
6HVD25RC	25000	2	25	500	400	25.00
Non-Tapped Subseries (Figure 4)						
6HV3RC	3000	2	3	500	400	3.25
6HV5RC	5000	2	5	500	400	3.25
6HV8RC	8000	2	8	500	400	3.25
6HV10RC	10000	2	10	500	400	5.50
6HV12RC	12000	2	12	500	400	5.50
6HV15RC	15000	2	15	500	400	7.00
6HV20RC	20000	2	20	500	400	10.25
6HV25RC	25000	2	25	500	400	13.25
6HV30RC	30000	2	30	500	400	16.25
6HV40RC	40000	2	40	500	400	25.00
6HV50RC	50000	2	50	500	400	25.00



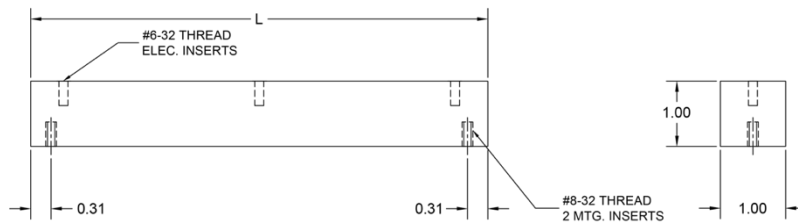
Temperature °C	
Operating Temperature	-55 to 150
Storage Temperature	-55 to 150
Maximum Junction Temperature	150

¹125°C ambient temperature unless stated otherwise.

²For solder turrets add suffix "T"; for mounting studs add suffix "S".

³Check Specification Definitions for conditions details.

Drawings



Dimensions in inches, tolerances ± 0.020 except as noted

Specification Definitions

Specifications		Conditions
V_{RRM}	Maximum Repetitive Reverse Voltage	Per Leg
I_{FAVM}	Maximum Average Forward Current	At $T_A = 55^\circ\text{C}$, Per Leg
V_F	Maximum Forward Voltage Drop	At I_{FAVM} , Per Leg
I_R	Maximum Leakage Current	At V_{RRM}
I_{FSM}	Maximum Surge Current	At 8.3 mS, Single Half Sine

Configurations:

Figure 1

Common Anode (Negative) Center Tap

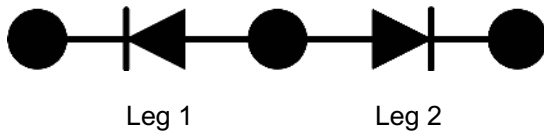


Figure 2

Common Cathode (Positive) Center Tap

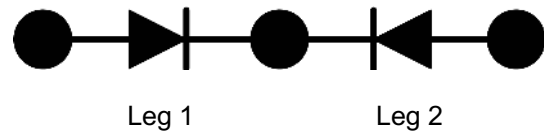


Figure 3

Doubler-Style Center Tap

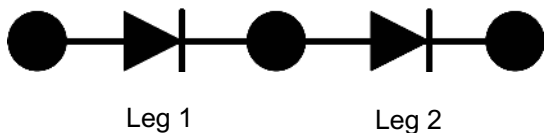


Figure 4

Non-Tapped



Note: Specifications subject to change without notice. Photo is representation only.

