## **FAST HIGH VOLTAGE TRANSISTOR SWITCHES**

## DESCRIPTION

The high-voltage switches described here have a fixed on-time and are ideal for use in fast pulse and discharge applications. In contrast to switches with variable ontime, switches with fixed on-time are widely immune against any feedback effect from switch output to control input. The pulse width is stable even under worst case conditions (bad circuit layout, long open wiring, magnetic coupling, undefined load etc.). Switches with fixed on-time have a very short rise and propagation delay time, which makes them ideal for pockels cell applications.

BEHLKE HTS switches are actively controlled devices (no avalanche technique) and show highly reliable and reproducible switching behaviour regardless of temperature, voltage or load condition. Compared to conventional high voltage switching elements, such as gas discharge tubes and spark gaps, BEHLKE solid-state switches do not show aging effects and achieve life times by several orders of magnitude higher than any other classical high voltage switch.

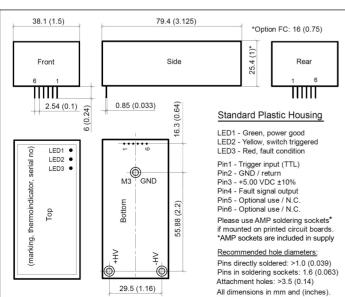
The switches are very easy to handle and only require a simple +5 VDC auxiliary supply (4.5 to 5.5 VDC) and a TTL-compatible trigger signal for the control. The trigger can be any positive going pulse of at least 25 ns duration and 2 to 10 volts amplitude. Due to the Schmitt-Trigger input characteristics and the very high signal amplification neither the switching behavior nor the turn-on rise time will influence by the waveshape of the trigger pulse. After being triggered, the switch turns on for about 100 nanoseconds. Longer lasting on-times are possible by means of the on-time extension options OT-1µs, OT-10µs and OT-100µs. Shorter on-times are realized by the on-time reduction options OT-25ns, OT-50ns and OT-75ns. Any other customized on-time between 25 ns and 100µs can be ordered under the option OT-C. The on-time can also be adjusted within certain limits by means of the option OT-P (programmable on-time). The recovery time after a switching cycle is less than 330 ns making burst frequencies of up to 3 MHz possible. Burst frequencies of up to 10 MHz can be achieved by means of option HFB.

The internal driving circuit provides signal conditioning, auxiliary voltage monitoring, frequency limitation, as well as driver and switch temperature protection. The operating conditions are indicated by three built-in LEDs. In case of a fault (auxiliary voltage < 4.5 VDC, frequency > f(max) and case temperature > 75°C), the red LED will indicate an error and the switch is inhibited for at least 2 seconds respectively for the duration of the fault condition. At the same time a TTL compatible fault signal occurs at pin 4 (Low = Fault). In case of over temperature the switch can be locked for several minutes, depending on the individual cooling conditions. A green LED indicates "Ready for Operation" and a yellow LED flashes if the switch has been triggered successfully.

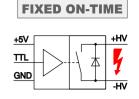
The standard plastic housing is the cost efficient solution in any low power / low frequency application with up to 5 watts power dissipation. For a power dissipation above 5 Watts there are various cooling options available. Those options include copper cooling fins for liquid immersion (option CF-LC), ceramic cooling fins for forced air (option CF-C), grounded cooling flange (potential-free) for classical heatsinks (option GCF), indirect liquid cooling for conductive water (option ILC) and direct liquid cooling with non-conductive coolants (e.g. Galden) for best cooling performance and lowest capacitive power losses at very high operating frequencies.

## **CIRCUIT DESIGN RECOMMENDATIONS**

In order to achieve the minimum turn-on rise time and the best HV pulse shape, all leads and circuit paths should be of lowest possible inductance. This can be achieved by means of very wide and short circuit tracks on the printed circuit board, if necessary in several layers (multi layer PCB). Part components such as RS, CBP and CB must be "inductance-free" and should only be connected with shortest possible wires / circuit tracks. Ground conducting tracks including the logic ground must be connected to a common ground point (star-type ground). Induction loop areas of dynamically current-carrying circuit paths should always be as small as possible. HV wiring and control circuitry should always be separated by a proper distance. For further design recommendations please refer to the general instructions.



HTS 40-06 4 kV / 60 A HTS 50-05 5 kV / 50 A HTS 80-03 8 kV / 30 A HTS 160-01 16 kV / 15 A



## 1ns Rise Time ● 5MHz Rep Rate 10MHz Burst ● tp=25ns...100µs

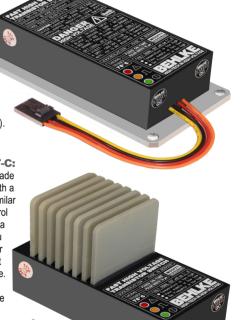
Option FC:
Flat case specially for printed circuit boards. The height is reduced from 25 to 16 mm.
The soldering pins can optionally be replaced by a pig tail with AMP-modu plug (opt. PT-C).
The HV connectors are located at the bottom side.

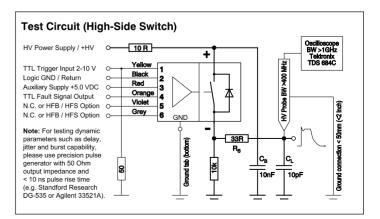
Option GCF / PT-C:
Grounded cooling flange
with pig tail and
AMP-modu plug for
control connection.
The HV connector
terminals are
at the front
side of module.
The HV connectors
can also be made as
flexible pig tails with
cable lugs (opt.PT-HV).

Option CF-C / PT-C: Isolated cooling fins made of special ceramics with a thermal conductivity similar to Aluminum. The control connection is made by a pig tail with AMP-modu plug. The HV connector terminals are at the front or bottom side of module. The V connectors can also be made as flexible pig tails with cable lugs

(option PT-HV).

For further options please refer to the catalog section B1 under www.behlke.com or consult BEHLKE directly.









SPECIFICATION	SYMB.	CONDITION / COM	IMENT			HTS 40-06	HTS 50-05	HTS 80-03	HTS 160-01	UNIT		
Maximum Operating Voltage	V <sub>O(max)</sub>		$T_{case}$ = 25 °C, $I_{off}$ < 100 $\mu$ ADC			4000	5000	8000	16000	VDC		
Minimum Operating Voltage	$V_{O(min)}$	$t_{\text{r(on)}}$ and $t_{\text{r(off)}}$ may in	$t_{\text{r(on)}}$ and $t_{\text{r(off)}}$ may increase sligthly if operated below 5% of $V_{\text{O(max)}}$					0				
Typical Breakdown Voltage	$V_{Br}$		Typical value (±5%), I <sub>off</sub> > 1mADC, T <sub>case</sub> = 70 °C					4400 5500 8800 17600				
Galvanic Isolation Voltage	VI	HV switch against c		20	000		VDC					
Maximum Peak Current	I <sub>P(max)</sub>	$T_{case} = 25  ^{\circ}C, t_{p} < 10$	$T_{case}$ = 25 °C, $t_p < 100 \mu s$ , DC 1%. Further SOA data on request.			60	50	30	15	ADC		
Static On-Resistance	R <sub>stat</sub>	$T_{case} = 25 \text{ °C}$ $ \begin{array}{c} 0.1 \text{ x } I_{P(max)} \\ 0  I_{P(max)} \end{array} $		` '	3.2 6.7	4.8 10.5	12.8 27	56 140	Ω			
Maximum Off-State Current	I <sub>off</sub>	T <sub>case</sub> =25°C, 0.8 x V <sub>0</sub>	o, low leakage (<1µA) option	nally av	ailable		1	10	1	μADC		
Turn-On Delay Time	t <sub>d(on)</sub>	Typical value (±5%)	, rising edges 50-50%, 0.8 x	κ V <sub>o.</sub> @	I <sub>P(max)</sub>	55			ns			
Typical Turn-On Rise Time	-On Rise Time $R_1 = 10kO$ , $C_1 = 10nF$ , $R_2 = 33O$ . The rise time $t_{con} = 0.2 \times V_{O(max)}$				0.2 x V <sub>O(max)</sub>	0.9	1.0	3.0	6			
(Output Pulse Rise Time)	$t_{r(on)}$	may increase by ap	prox. 10-30% with options (	T-xn.	0.8 x V <sub>O(max)</sub>	1.4	1.5	3.9	15	ns		
Tunical Turn Off Dica Time	$t_{r(off)}$	Standard devices					2	20				
Typical Turn-Off Rise Time		Resistive load	Devices with on-time reduction options (OT-xn)				1	10				
(Output Pulse Fall Time)		Devices with on-time extension options (OT- $x\mu$ )					$t_{r(off)}$	$\approx t_{on}$	Note 1)	ns		
	t <sub>on</sub>	Standard devices						00		ns		
On-Time		Resistive load, Devices with on-time redu		uction options (OT-xn)			25 / 50 / 75 or customized			ns		
		50-50% Devices with on-time exte			ptions (OT-xµ)		1 / 10 / 100 or customized Note 1)		μs			
Cuitab Dagguery Time	t <sub>rc</sub>	Triagas pulsa width	-E0πο	Standard devices		330						
Switch Recovery Time		Trigger pulse width <50ns Option HFB, I-HFB			100				ns			
Typical Turn-On Jitter	t <sub>j(on)</sub>	V <sub>aux</sub> = 5.0 V, V <sub>tr</sub> = 5.	0 V, t <sub>rtr</sub> < 10 ns				100					
Max. Continuous Switching	f <sub>(max)</sub>		quency dependent power	Stand	ard devices	İ	0.	.12		ps		
Frequency		dissipation. Cooling	options may be required.	Option	HFS + DLC			5		MHz		
Maximum Burst Frequency	f <sub>b(max)</sub>	@ t <sub>ptr(min)</sub> . Please se	lect an adequate on-time	Stand	ard devices		3					
		option when generating high frequency bursts. Option HFB, I-HFB				10				MHz		
Maximum Number of		Option I-HFB / HFB	is recommended for >100	Stand	ard devices	300						
Pulses / Burst	In I		sure a constant t <sub>r(on).</sub>	Option	n HFB, I-HFB	Only limited by buffer capacitance and temperature.				Pulses		
		Standard plastic ca	se, forced air >4m/s, T <sub>case</sub> =	25 °C		5						
	P <sub>d(max)</sub>	Devices with opt. CF-C, ceramic fins in forced air, >4m/s, T <sub>fin</sub> = 25°C				32						
		Devices with opt. GCF (grounded cooling flange), T <sub>flange</sub> = 25°C					1	00				
Maximum Power Dissipation			Devices with opt. ILC (indirect liquid cooling), water 1l/min, T <sub>inlet</sub> = 25°C				1	00				
		Devices with opt. CF-LC, Cu fins in Galden®, >0.1m/s, T <sub>fin</sub> = 25°C				192						
		Devices with opt. DLC, Galden® coolant, flow 3 l/min, T <sub>inlet</sub> = 25°C				1500				Watts		
		Above Standard plastic case Option CF-C (ceramic cooling fins in forced air)				0.11						
							0.71					
L: 5 "		T <sub>case</sub> = 25°C Option GCF (grounded cooling flange)				2.22						
Linear Derating		T <sub>fin</sub> = 25°C	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1				2.22					
		T <sub>flange</sub> = 25°C Option CF-LC (Cu cooling fins in forced Galden®)				4.27 42.9 (To for option DLC is <60°C)						
		T <sub>inlet</sub> = 25°C Option DLC (direct liquid cooling with Galden®)								W/K		
Operating Temperature Range	To	Extended temperature range on request				-4070				°C		
Storage Temperature Range	$T_{ST}$						-50	90		°C		
Natural Capacitance	C <sub>N</sub>	Capacitance of the switching path (MOSFET capacitance) @ V <sub>O(max)</sub>				35	35	35	12	pF		
Coupling Capacitance	Cc	Stray capacitance between HV side and grounded control side					<	(Options I	LC / GCF: <30pF)	pF		
Diode Reverse Recovery Time	t <sub>rrc</sub>	Recovery time of intrinsic MOSFET diode @ I <sub>F</sub> = 0.2 x I <sub>P(max)</sub>				500						
Max. Ambient Magnetic Field	В	Homogeneous stea	dy-field, surrounding the wh	ole swit	ching module	25				mT		
Aux. Supply Voltage Range	V <sub>aux</sub>	V <sub>aux</sub> has <b>no</b> impact	4.5 to 5.5				VDC					
Auxiliary Supply Current	l <sub>aux</sub>	Typical value ( $\pm 10\%$ ), @ $V_{aux} = 5.0 \text{ V}$ , $T_{case} = 25^{\circ}\text{C}$ . @ f < 100 Hz				60						
		I <sub>aux</sub> is a linear function	500				mADC					
Trigger Signal Voltage Range		$I_{aux}$ is a linear function of the operating frequency. @ f = 100 kHz 3 to 5 V recommended for low jitter. $V_{tr} > 5.5 V$ will be clamped.				2-10				VDC		
	V <sub>tr</sub>	3 to 5 V recommend	aed for fow fitter. V <sub>tr</sub> ≥5.5V W	Note: TTL trigger input is equipped with protection and filter network.					3.3			
Trigger Input Impedance					filter network.		3	ა.ა		kΩ		
	Z <sub>tr</sub>	Note: TTL trigger in	put is equipped with protect	ion and				25		ns		
Minimum Trigger Pulse Width	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha	put is equipped with protect as <b>no</b> impact on the dynamic	ion and c switch	ing behavior.		2			<del>                                     </del>		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time	Z <sub>tr</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic	put is equipped with protect	ion and c switch nput ch	ing behavior. aracteristics.		2	25		ns		
Minimum Trigger Pulse Width	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic. "L" indicates switch	put is equipped with protect as <b>no</b> impact on the dynamic al due to "Schmitt Trigger" i over temperature (>75°C /	ion and c switch nput cha 167°F),	aracteristics.  "H" signal		2	25		ns		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove	put is equipped with protect as <b>no</b> impact on the dynamic al due to "Schmitt Trigger" i	ion and c switch nput cha 167°F), apply.	ing behavior. aracteristics.		0.	25  5		ns ns VDC		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time Fault Signal Output Voltage Fault Signal Output Current	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove Source and sink cur	put is equipped with protect is <b>no</b> impact on the dynamic all due to "Schmitt Trigger" in over temperature (>75°C / or frequency and low aux. surrent, output short circuit pro-	ion and c switch nput ch 167°F), ipply.	aracteristics.  "H" signal		0.	25 5 01		ns ns VDC		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time Fault Signal Output Voltage	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove Source and sink cur Switch cannot be da	put is equipped with protect as <b>no</b> impact on the dynamic all due to "Schmitt Trigger" i over temperature (>75°C / or frequency and low aux. surrent, output short circuit proamaged by false control control control control in the second control control control is not second control control in the second control is not second control in the second control in the second control is not second control in the second control is not second control in the second control in the second control is not second control in the second control in the second control is not second control in the second control in the second control is not second control in the second con	ion and c switch nput changed 167°F), apply. pof. aditions.	ing behavior. aracteristics. "H" signal "L" signal		0.	25 5 01 10	<b>1</b> "	ns ns VDC mADC		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time Fault Signal Output Voltage Fault Signal Output Current Fault Detector Response Time	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove Source and sink cur Switch cannot be da Green LED, illumina	put is equipped with protect as <b>no</b> impact on the dynamic all due to "Schmitt Trigger" i over temperature (>75°C / or frequency and low aux. surrent, output short circuit proamaged by false control contated continuosly in normal o	ion and c switch nput cha 167°F), apply. oof. ditions.	ing behavior. aracteristics.  "H" signal "L" signal		0. 1 <1 ,Ready / auxilia	25 5 01 10 100 ary power good		ns ns VDC mADC		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time Fault Signal Output Voltage Fault Signal Output Current	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove Source and sink cur Switch cannot be da Green LED, illumina Yellow LED, illumina	put is equipped with protect is <b>no</b> impact on the dynamic all due to "Schmitt Trigger" is over temperature (>75°C / or frequency and low aux. surrent, output short circuit proamaged by false control contated continuosly in normal of ated for 20 ms if a valid triggent in the dynamic and the sequence of the sequence o	ion and c switch nput change the 167°F), apply. oof. ditions. peration ger is ap	ing behavior. aracteristics.  "H" signal "L" signal		0. 1 <1 ,Ready / auxilia "Switch succe	25 5 01 10 100 ary power good sfully triggered	ш	ns ns VDC mADC		
Minimum Trigger Pulse Width Max. Trigger Pulse Rise Time Fault Signal Output Voltage Fault Signal Output Current Fault Detector Response Time	Z <sub>tr</sub> t <sub>ptr(min)</sub>	Note: TTL trigger in The trigger pulse ha Slew rate is uncritic "L" indicates switch driver overload, ove Source and sink cur Switch cannot be da Green LED, illumina Yellow LED, illumina	put is equipped with protect as <b>no</b> impact on the dynamic all due to "Schmitt Trigger" i over temperature (>75°C / or frequency and low aux. surrent, output short circuit proamaged by false control contated continuosly in normal o	ion and c switch nput change the 167°F), apply. oof. ditions. peration ger is ap	ing behavior. aracteristics.  "H" signal "L" signal		0. 1 Ready / auxilia "Switch succesult / switch is l	25 5 01 10 100 ary power good sfully triggered	ш	ns ns VDC mADC		

Note 1) Due to their relatively slow turn-off rise time / pulse fall time ( $t_{r(off)} \approx t_{on}$ ), devices with on-time extension options OT-1 $\mu$ , OT-10 $\mu$  and OT-100 $\mu$  should not be used in hard switching applications! Ordering Information (for further options please refer to the product survey B1 of the on-line catalog)

HTS 40-06	Transistor switch, 4000 VDC, 60 A, 100 ns on-time	Option HFS	High frequency switching (>120kHz). Connectors for external driver supply (+15 VDC, +280 VDC, 0.1 mA/kHz)				
HTS 50-05	Transistor switch, 5000 VDC, 50 A, 100 ns on-time	Option UL	Flame retardant casting resin, UL94-V0 (option refers to the resin only, the housing is always UL-94-V0 conform)				
HTS 80-03	Transistor switch, 8000 VDC, 30 A, 100 ns on-time	Option FC	Flat case, housing dimensions 79.5 x 38 x 16 mm <sup>3</sup> instead of 79.5 x 38 x 25 mm <sup>3</sup> (no cooling options available)				
HTS 160-01	Transistor switch, 16000 VDC, 15 A,100 ns on-time	Option CF-C	Cooling fins made of highly heat conductive ceramics. Designed for forced air convection with air flow > 4m/s.				
Option OT-1u	On-time extension to approx. 1 µs (-5%, +30%)	Option CF-LC	Cooling fins optimized for liquids. Immersion in non-conductive liquids only (mineral oil, silicone oil or Galden®).				
Option OT-10u	On-time extension to approx. 10 µs (-5%, +30%)	Option GCF	Grounded cooling flange for classical heatsinks. The stray capacitance (C <sub>C</sub> ) will be increased to 30 pF.				
Option OT-100u	On-time extension to approx. 100 µs (-5%, +30%)	Option ILC	Indirect liquid cooling for conductive liquids such as water. The stray capacitance (Cc) will be increased to 30pF.				
Option OT-25n	On-time reduction to approx. 25 ns (-5%, +10%)	Option DLC	Direct liquid cooling for non-conductive liquids (e.g.Galden HT135). For high frequency operation. To(max)=60°C				
Option OT-50n	On-time reduction to approx. 50 ns (-5%, +10%)	Option PT-C	Pigtails for control connection instead of pins. Self-latching AMP-modu plug. Recommended if not used on PCB's.				
Option OT-75n	On-time reduction to approx. 75 ns (-5%, +10%)	Option PT-HV	Pigtails for HV at the front side instead of the screw terminals at the bottom. Good for free wiring (no PCB design).				
Option OT-C	Customized on-time, please indicate demanded on-time with order	Option I-PC	Integrated part components (R, C, D etc.) according to customers specification. Additional components must fit in empty space.				
Option HFB	High frequency burst. Reduced recovery time + buffer connector	Option PC	Pulser configuration. The PC configuration includes buffer caps., working and damping resistors, EMC filters and HV sockets.				
Further technical data ar	urther technical data and mechanical drawings are available on request. All data and specifications subject to change without notice. Please consult BEHLKE for custom designed switches and pulsers.  160-01-TB-REV-28.09.11						