



Schottky Barrier Diode	Reverse Voltage - 40 Volts Forward Current - 3.0 Amperes	
FEATURES •Low profile package. •Ideal for automated placement. •Low forward voltage drop. •High surge capability. •Guard ring for overvoltage protection •Comply with AEC-Q101	.065 (1.65) .049 (1.25) .181 (4.60) .157 (4.00)	
 MECHANICAL DATA Case: SMA, molded plastic. Terminals: Lead free plating (Tin finish) Solderable per MIL-STD-202, Method 208. Epoxy: UL flammability classification rate 94V-0. Polarity: Cathode band. Weight: 0.062 grams(approx.). 	.103 (2.62) .079 (2.00) .060 (1.52) .030 (0.76) .208 (5.28) .188 (4.80)	

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

CHARACTERISTICS	SYMBOL	SS34A	UNIT
Marking		340	
Repetitive peak reverse voltage	VRRM	40	V
RMS voltage	VRMS	28	V
DC blocking voltage	VDC	40	V
Average forward rectified current	lF	3.0	А
Peak forward surge current 8.3ms single half sine-wave	IFSM	80	А
superimposed on rated load (JEDEC method)			
Forward voltage IF = 3A	VF	0.55	V
Reverse current VR =VRRM, TA=25°C	lr	0.5	mA
Junction capacitance (Note 1)	CJ	150	pF
Thermal resistance Junction to ambient	Reja	50	°C/W
Thermal resistance Junction to case	Rejc	30	°C/W
Operating temperature range	TJ	-55 to +125	°C
Storage temperature range	Тѕтс	-55 to +150	°C

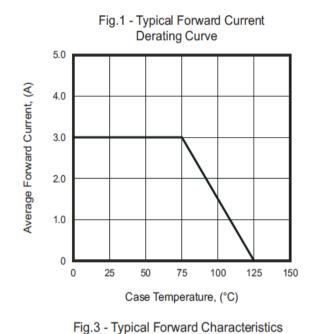
Note 1.Measured at 1.0MHZ and applied reverse voltage of 4.0V DC.

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Dimensions in inches and (millimeters)

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Peak Forward Surge Current, (A)

Forward Surge Current

8.3 ms Single Half Sine-Wave (JEDEC Method)

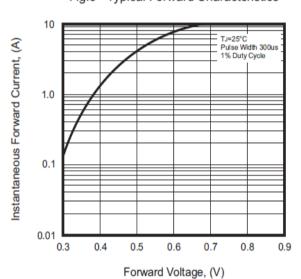
T.=25 °C

1 10 100

Fig.2 - Maximum Non-Repetitive

Number of Cycles at 60Hz

Fig.4 - Typical Reverse Characteristics



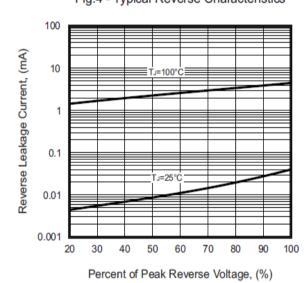
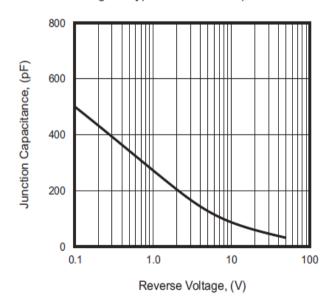


Fig.5 - Typical Junction Capacitance



The curve above is for reference only. 曲线图仅供参考。

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